Please write clearly in block capitals.								
Centre number				Candidate number				
Surname								
Forename(s)								
Candidate signat	ure							

AS COMPUTER SCIENCE

Paper 2

Friday 8 June 2018

Morning

Time allowed: 1 hour 30 minutes

Materials

For this paper you must have:

• a calculator.

Instructions

- Use black ink or black ball-point pen.
- Fill in the boxes at the top of this page.
- Answer **all** questions.
- You must answer the questions in the spaces provided. Do not write outside the box around each page or on blank pages.
- Do all rough work in this book. Cross through any work you do not want to be marked.

Information

- The marks for questions are shown in brackets.
- The maximum mark for this paper is 75.

Advice

- In some questions you are required to indicate your answer by completely shading a lozenge alongside the appropriate answer as shown.
- If you want to change your answer you must cross out your original answer as shown.
- If you wish to return to an answer previously crossed out, ring the answer you now wish to select as shown.



TOTAL



		An	swer all questions in the spaces provided.		Do not write outside the box
0 1	Table 1 d	lescribe	s some sets of numbers.		
			Table 1		
		Α	A set of numbers that represent all possible real world quantities.		
		В	A set of numbers that can be written as fractions (ratios of integers).		
		С	A set of numbers that cannot be written as fractions (ratios of integers).		
0 1.1	Shade in set of rea	one loz I numb	enge to indicate which of the descriptions in Table 1 descriers.	bes the	
	A \bigcirc	В	○ C ○	[1 mark]	
0 1.2	Shade in set of irra	one loz tional n	enge to indicate which of the descriptions in Table 1 descri umbers.	bes the	
	A \bigcirc	В	○ C ○	[1 mark]	2



<u>0 2 . 1 </u> Table	∠ lists tive differe	ent quantities of mer	nory, each measur	ea using diffe	rent units.
Place Posit i repres	the quantities of r on column of Tal senting the larges	memory into order t ole 2, with 1 represe t quantity.	by writing the numb enting the smallest	ers 1 to 5 in tl quantity and {	ne 5
					[2 marks]
		Tat	ole 2		
	Ľ	Quantity	Position		
	;	3 kilobytes			
	:	2 mebibytes			
	:	2 bytes			
	:	2 megabytes			
	1	20 bits			
0 2.2 Conve togeth the ar You n	ert the hexadecin ler to work out the lswer. 1ust show your w	nal numbers 27 and e total. Finally, conv orking.	C9 into binary . Thert the total back in	ien, in binary to hexadecin	, add them nal to give [2 marks]
0 2.2 Convertogeth togeth the ar You n	ert the hexadecin ler to work out the lswer. nust show your w	nal numbers 27 and e total. Finally, conv orking.	C9 into binary . Thert the total back in	ien, in binary to hexadecin	, add them nal to give [2 marks]
0 2.2 Convertogeth the ar You n	ert the hexadecin her to work out the iswer. 1ust show your w	nal numbers 27 and e total. Finally, conv orking.	C9 into binary . Thert the total back in	ien, in binary to hexadecin	, add them nal to give [2 marks]
0 2.2 Convertogeth the ar You n	ert the hexadecin her to work out the hower. hust show your w	nal numbers 27 and e total. Finally, conv orking.	C9 into binary . Thert the total back in	ien, in binary to hexadecin	, add them nal to give [2 marks]



03.1	Describe the difference between analogue and digital data. [2 marks]	Do not write outside the box
	Two methods of representing music digitally are as sampled sound and using MIDI.	
03.2	State two advantages of representing music using MIDI instead of as sampled sound. [2 marks]	
		4



4.2 A sensitive message could be encrypted using either the Vernam cipher or the Caesar cipher. Explain why the Vernam cipher is a better choice. [2 marks]	0 4.1	What is encryption? [1 mark]
The bit pattern 1010011 1001111 1001110 represents the string 'SON' in 7-bit ASCII. The bit pattern 1000001 represents the character 'A' in 7-bit ASCII and other characters follow on from this in sequence. For example, the bit pattern 1001000 represents the character 'H'. 4. 3 What bit pattern results from encrypting the string 'SON' using a Vernam cipher with the key 'HOG'? You must show your working. [3 marks]	94.2	A sensitive message could be encrypted using either the Vernam cipher or the Caesar cipher. Explain why the Vernam cipher is a better choice. [2 marks]
 The bit pattern 1010011 1001111 1001110 represents the string 'SON' in 7-bit ASCII. The bit pattern 1000001 represents the character 'A' in 7-bit ASCII and other characters follow on from this in sequence. For example, the bit pattern 1001000 represents the character 'H'. What bit pattern results from encrypting the string 'SON' using a Vernam cipher with the key 'HOG'? You must show your working. 		
 4. 3 What bit pattern results from encrypting the string 'SON' using a Vernam cipher with the key 'HOG'? You must show your working. 		The bit pattern 1010011 1001111 1001110 represents the string 'SON' in 7-bit ASCII. The bit pattern 1000001 represents the character 'A' in 7-bit ASCII and other characters follow on from this in sequence. For example, the bit pattern 1001000 represents the character 'H'.
You must show your working. [3 marks]	4.3	What bit pattern results from encrypting the string 'SON' using a Vernam cipher with the key 'HOG'?
		You must show your working. [3 marks]



0 5	An operating system is a type of software.	Do not write outside the box
0 5.1	Shade one lozenge to indicate which category of software an operating system belongs to.	
	[1 mark]	
System	software O Translation software O	
0 5.2	State one resource that the operating system manages. [1 mark]	
0 5.3	State one role of the operating system, other than resource management. [1 mark]	
		3



06.1	What is the stored program concept? [2 marks]
	Ella writes a program on her home computer and compiles it into an executable file.
0 6.2	Ella's executable file will not run on Josephine's computer because the two computers have different processors.
	Explain why having different processors may have caused this problem. [2 marks]
	Turn over for the next question



Turn over ►

Do not write outside the box

	The processor in Ella's computer has four cores running at 2.8 GHz and the processor in Josephine's computer has one core running at 3.2 GHz.	Do not write outside the box
06.3	Considering these differences, explain why Josephine's computer might be able to complete a particular task more quickly than Ella's. [2 marks]	
		6



0 7	A company employs a team of programmers to develop software to control a fleet of	Do not write outside the box
	driverless cars, providing a taxi service for clients in a large city.	
0 7.1	Discuss a range of moral, ethical, legal and cultural issues that the programmers may need to consider whilst developing the service and that may arise during the use of the service by the public.	
	In your answer you will be assessed on your ability to follow a line of reasoning to produce a coherent, relevant and structured response. [9 marks]	
		J

Turn over ►

IB/M/Jun18/7516/2





07.2	Cameras within the taxi take still images once every second for security purposes. The images are compressed using run-length encoding and stored on a flash memory card within the camera.	Do not write outside the box
	Describe how a digital image could be captured by a digital camera and compressed using run-length encoding.	
	[6 marks]	
		6
	Turn over ►	



IB/M/Jun18/7516/2

0 8	A network with a physical star topology can have a logical bus topology.	
0 8.1	Describe the difference between a physical and a logical topology.	[2 marks]
08.2	Explain the operation of a physical star topology.	[2 marks]



Do not write outside the box

		Do not write
	A new bank is setting up an internal network.	outside the
08.3	Two types of networking are client-server and peer-to-peer. With reference to the needs of the bank and the properties of the two types of networking, explain why the bank should implement a client-server rather than a peer-to-peer network.	
	[6 marks]	
		10











1 0 Table 3 – standard	d AQA assembly language instruction set. This should be used			
to answer question part 10 . 1				
IDP Pd (momenty ref)	L and the value stored in the memory location specified by			
LDK KG, <memory rer=""></memory>	<pre>cload the value stored in the memory location specified by </pre>			
STR Rd, <memory ref=""></memory>	Store the value that is in register d into the memory location			
	<pre>specified by <memory ref="">.</memory></pre>			
ADD Rd, Rn, <operand2></operand2>	Add the value specified in <operand2> to the value in register</operand2>			
	n and store the result in register d.			
SUB Rd, Rn, <operand2></operand2>	Subtract the value specified by <operand2> from the value in</operand2>			
	register n and store the result in register d.			
MOV Rd, <operand2></operand2>	Copy the value specified by <operand2> into register d.</operand2>			
CMP Rn, <operand2></operand2>	Compare the value stored in register n with the value specified			
	by <operand2>.</operand2>			
B <label></label>	Always branch to the instruction at position <label> in the</label>			
	program.			
B <condition> <label></label></condition>	Branch to the instruction at position <label> if the last</label>			
	comparison met the criterion specified by <condition>.</condition>			
	Possible values for <condition> and their meanings are:</condition>			
	EQ: equal to NE: not equal to			
	GT: greater than LT: less than			
AND Rd, Rn, <operand2></operand2>	Perform a bitwise logical AND operation between the value in			
	register n and the value specified by <operand2> and store</operand2>			
	the result in register d.			
ORR Rd, Rn, <operand2></operand2>	Perform a bitwise logical OR operation between the value in			
	register n and the value specified by <operand2> and store</operand2>			
	the result in register d.			
EOR Rd, Rn, <operand2></operand2>	Perform a bitwise logical XOR (exclusive or) operation between			
	the value in register n and the value specified by <operand2></operand2>			
	and store the result in register d.			
MVN Rd, <operand2></operand2>	Perform a bitwise logical NOT operation on the value specified			
	by <operand2> and store the result in register d.</operand2>			
LSL Rd, Rn, <operand2></operand2>	Logically shift left the value stored in register n by the number			
	of bits specified by <operand2> and store the result in register</operand2>			
	d.			
LSR Rd, Rn, <operand2></operand2>	Logically shift right the value stored in register n by the number			
	of bits specified by <operand2> and store the result in register</operand2>			

Labels: A label is placed in the code by writing an identifier followed by a colon (:). To refer to a label, the identifier of the label is placed after the branch instruction.

d.

Interpretation of <operand2>

Stops the execution of the program.

<operand2> can be interpreted in two different ways, depending on whether the first character is a # or an R:

- # Use the decimal value specified after the #, eg #25 means use the decimal value 25.
- Rm Use the value stored in register m, eg R6 means use the value stored in register 6.

The available general purpose registers that the programmer can use are numbered 0 to 12.



HALT

			Do not write		
	Figure 3 shows an incomplete assembly language program, intended to perform integer division by 10.				
	The program decrements the value in R1 in steps of 10 until the value stored in R1 is less than 10. Each time that the value in R1 is decreased by 10 the value in R3 is increased by 1. For example, if R1 started at 43 the sequence of numbers stored in R1 would be 43, 33, 23, 13, 3 and the final value in R3 would be 4.				
10.1	Complete the program in Figure 3 .				
	You should assume that R1 has already been assigned a value to divide.				
	You may not need to use all four lines for your solution and you should not write more than one instruction per line. [4 marks]				
	1	Figure 3			
		MOV R3, #0			
	loopstart:	CMP R1, #10			
	end:	HALT			
	Turn over for the next question				
l			I		



	A processor supports 32 different basic n modes represented by a single bit, as sh	nachine code operations, and two addressing own in Figure 4 below.			
	Figure 4				
10.2	OpcodeBasic machine operationAddressing mode000111How many different opcodes is the mach	Operand 0 0 1 1 0 1 ine potentially capable of supporting? [1 mark]			
10.3	In direct addressing, the value stored in t location which contains the data to proce In direct addressing mode, how many me the instruction format described in Figure	he operand is the address of the memory ss. emory locations could a processor that used a 4 potentially make use of? [1 mark]			
1 1 1 1.1	Some compilers produce intermediate co whilst others produce executable code. Explain why some compilers produce by	de such as bytecode as their final output recode as the final output instead of			
		[1 mark]			



11.2	Describe how bytecode programs are executed after the bytecode has been produced.	Do not write outside the box		
	[2 marks]			
11.3	Explain what is meant by the term imperative high-level language. [2 marks]			
		5		



Turn over ►

1 2	Figure 5 shows a bitmap representation of an image consisting of white, red, blue, black and yellow pixels only.	Do not write outside the box			
	Figure 5				
12.1	Calculate the minimum size of file (excluding metadata) that could be used to store the bitmap image in Figure 5 . Express your answer in bytes.				
	You must show your working. [3 marks]				
12.2	Shade in one lozenge to indicate the minimum colour depth in bits required for an image with 18 colours.				
	[1 mark]				
	3 0 4 0 5 0				
		4			
END OF QUESTIONS					
Copyright © 2018 AQA	and its licensors. All rights reserved.				

